

PLTW | Engineering

Digital Electronics

Course Outline

1

Course Location:	B211 STEM Lab
Suggested Resources:	A 3-ring binder, not provided, is recommended to organize notes, worksheets, and other technical documents as necessary. Writing utensil.
Instructor's Name:	Ed Sayre
Contact Telephone:	856-451-9400 x272, 1PM-2PM, M-F
CRHSD E-mail Address:	sayre@crhsd.org [Best way to contact me]

Welcome!

Greetings, and welcome to Digital Electronics, the second STEM course offered through Project Lead the Way at Cumberland Regional High School. I am very delighted to be your son/daughter's instructor for this class, and my hope is that he/she will find this course to be challenging yet rewarding and interesting as we explore combinational and sequential logic design. Below and attached you will find additional information regarding course policies, expectations and a course outline. Please do not hesitate to contact me with any questions or concerns.

GRADING PROCEDURES

In an effort to maximize the use of the integrated tools of the PLTW program, grading will take place in the myPLTW Learning Management System(LMS) available at <https://my.pltw.org>. Grades, once posted, will be viewable just as they are in Genesis. Please note that Genesis will not have any grades listed for this course except for progress reports and report cards. Access to the LMS can only be granted by logging on using student credentials; any credentials provided for Genesis are not related and will not work.

Grading will take place using common rubrics for each project as determined by the project details. Students will be able to access rubrics for projects throughout the course.

GRADE CALCULATION

Grades are calculated using the following weights in three categories:

- Homework/Classwork: 25%
- Tests/Projects: 50%
- Quizzes: 25%

Homework will be graded as follows:

Completed and on-time: 100%
Incomplete or 1 day late: 50%
More than 1 day late: 0%